

Guidelines for Authors Submitting Articles on Design and Implementation of Signal Processing Systems

by the *IEEE Signal Processing Society Technical Committee on Design and Implementation of Signal Processing Systems*

The IEEE Signal Processing Society welcomes high quality articles and tutorials of theoretical or practical emphasis that relate to design and implementation of signal processing systems in any domain, such as audio, image, video, and telecom processing.

This document describes the primary options available for submitting papers on design and implementation to publications of the IEEE Signal Processing Society.

The **IEEE Signal Processing Letters (SPL)** welcomes short, high-quality papers on signal processing architectures and VLSI hardware, DSP algorithm implementation, and tools. The aim of SPL is to publish fresh, novel ideas that have not been presented before and merit fast publication.

The **IEEE Signal Processing Magazine** publishes tutorial-style articles on signal processing research and applications, as well as columns and forums on issues of interest. Its coverage ranges from fundamental principles to practical implementation. Authors may submit papers to special issues that relate to selected themes on active or emerging areas. Authors may also submit contributed articles that provide comprehensive reviews and surveys on topics that are not necessarily connected to any of the active calls for special issue papers.

A submission to the **IEEE Transactions on Signal Processing (TSP)** may be a regular paper (a paper that provides a complete disclosure of a technical premise), or a correspondence (a brief item that describes a use for or magnifies the meaning of a single technical point, or provides comment on a previously-published TSP paper). Suggested topics relating to design and implementation include:

- Algorithm and architecture co-optimization
- Compilers and tools for DSP implementation
- DSP algorithm implementation in hardware and software
- Low-power signal processing techniques and architectures
- Programmable and reconfigurable DSP architectures
- System-on-chip architectures for signal processing

As with all sub-areas of the IEEE Signal Processing Society, the relationship to signal processing should be addressed clearly in any submitted paper.

For further information, including links to these publications and full details on their submission requirements, we refer interested authors to the web page of the IEEE Signal Processing Society:

<http://www.ieee.org/organizations/society/sp/>